

ABSTRACT OF THE DISCLOSURE

A semiconductor memory device includes a plurality of input/output terminals, a memory cell array which are divided into blocks respectively
5 corresponding to the input/output terminals such that only one of the blocks corresponds to a given one of the input/output terminals, sense amplifiers, which are connected to the blocks at a side thereof, and amplify data of the memory cell array, switches
10 which are respectively connected to the sense amplifiers, and signal lines, which connect the sense amplifiers to a corresponding one of the input/output terminals via the switches.